

BOUNDARY SURFACE AND LOAD PLANE OF THE TERNARY MEMORY

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The item deals with the impact of the load plane on circuit of multiple valued memory. If the multiple-value memory consists of two one-peak resonant tunnel diodes, change in the number of singularities can be achieved by resizing R without additional circuit modifications. This change will become evident by change of the boundary surface morphology, which even without calculating their eigenvalues in Matlab indicates stability or instability of newly formed singularities. When I - V surfaces of both elements multiple intersect due to $R > 0$, there can occur such case, that the stable singularities will be less than unstable, what cannot be achieved when $R = 0$.

Key words: state space, load plane, singularity, ternary memory, stable limit cycle, resonant tunnel diode.

Introduction. References [1] - [5] show analysis of multiple-valued logic memory which consist of two series connected tunnel diodes or resonant tunnel diodes (fig.1) when $R = 0$. The results of the investigation were surprising because of the presence of undesirable SLC, boundary surface (BS) fragmentation, and also presence of a saddle virtual singularity [6], [7] which makes it impossible controlling memory [8]. Work [9] pointed out the change in the number of singularities when parameter R is changed. Thus it was possible to reach from the original five singularities (fig.1), only three (binary memory-fig.2), or even seven singularities. This would mean that only by a change of R , without changing the I - V characteristics, ternary memory can be changed to binary or quaternary memory. As in works [1-5] for $R = 0$, also in these cases for $R > 0$ were made cross-sections of BS with surprising results mentioned in this article. Multiple valued memory can be used also in modern production technologies [10] - [13].

Circuit and its description. The analysed circuit in fig.1 is described by the system of state equations (1). The $i_2(u_2)$ and $i_1(u_1)$ represent the piecewise-linear (PWL) characteristics of the element and the load, respectively given by the expression (2)

$$\begin{aligned} L \left(\frac{di}{dt} \right) &= U - Ri - (u_1 + u_2) \equiv Q_1 \\ C_1 \left(\frac{du_1}{dt} \right) &= i - i_1(u_1) \equiv Q_2 \\ C_2 \left(\frac{du_2}{dt} \right) &= i - i_2(u_2) \equiv Q_3 \end{aligned} \quad (1)$$

$$\begin{aligned} i_j(u_j) &= \frac{1}{2} ({}^j g_0 + {}^j g_3) u_j + \frac{1}{2} [({}^j g_1 - {}^j g_0) |u_j - U_1| + \\ & ({}^j g_2 - {}^j g_1) |u_j - U_2| + ({}^j g_3 - {}^j g_2) |u_j - U_3|] - \\ & \frac{1}{2} [({}^j g_1 - {}^j g_0) U_1 + ({}^j g_2 - {}^j g_1) U_2 + ({}^j g_3 - {}^j g_2) U_3] \end{aligned} \quad (2)$$

where ${}^j g_m$ are the conductances of the segments of the PWL characteristics and ${}^j U_k$ are the break points. Here $m = 0, 1, 2, 3$ and $k = 1, 2, 3$. Indexes $j=1$ and $j=2$ correspond to the load and the element, respectively.

The parameter values corresponding to element and load device are as follows: ${}^1 g_0 = 0,0833$; ${}^1 g_1 = -0,0571$; ${}^1 g_2 = 0$; ${}^1 g_3 = 0,0281$; ${}^2 g_0 = 0,1$; ${}^2 g_1 = -0,05$; ${}^2 g_2 = 0$; ${}^2 g_3 = 0,032$ [S]; ${}^1 U_1 = 60$; ${}^1 U_2 = 130$; ${}^1 U_3 = 280$; ${}^2 U_1 = 50$; ${}^2 U_2 = 140$; ${}^2 U_3 = 260$ [mV]. (3)

The bias voltage of the circuit is $U = 440$ mV and control pulse $\Delta I = 0$.

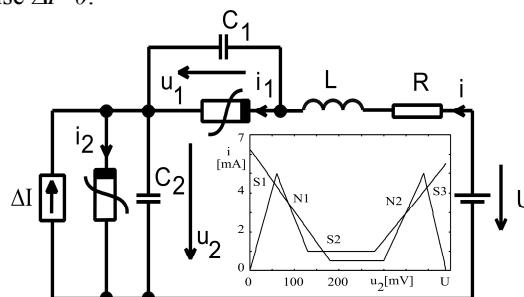


Figure 1 - Model of the analysed circuit and projection of I - V characteristics and singularities into plane i, u_2

Influence of resistance R on the character of singularities. Geometric interpretation of resistivity $R > 0$ from work [6] is presented in fig.2.

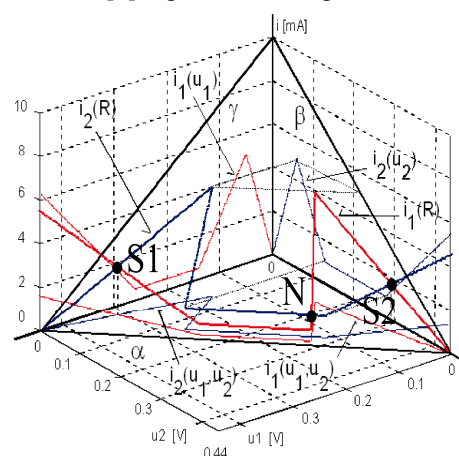


Figure 2 - Traces of load plane α, β, γ , which respond to equation $Q_i = 0$ (1). Singularities S_1, N, S_2 lies on load plane $Q_i = 0$. The parameter values of $i_1(u_1)$ and $i_2(u_2)$ is in the previous text, with $R = 44 \Omega$

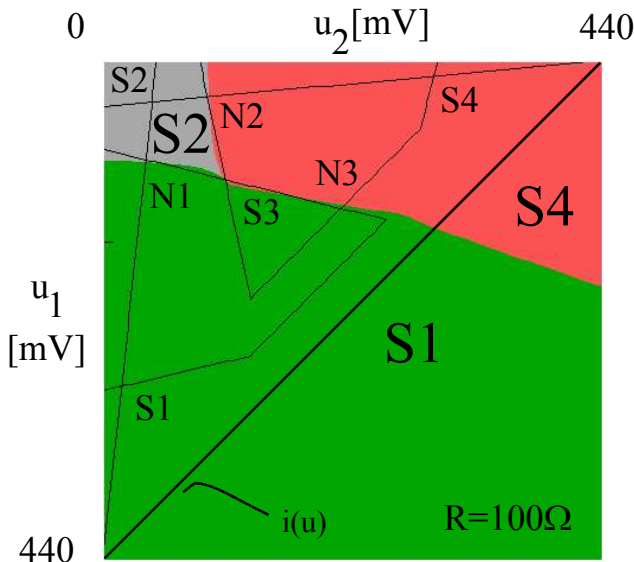


Figure 3 Cross-section of BS for current level corresponding to current level of singularity $S3$ ($S3i=2.28mA$). Denotation $i(u)$ represents trace of load plane when $R=0$, or trace α when $R=100\Omega$.

For $R=100\Omega$, $L=1e-10H$ and $C=C1=C2=2,6e-13F$ was obtained even seven singularities. So far, there always has been followed the change a sequence of singularities in the physical circuits: stable - unstable - stable etc. This was true for memories with positive and also negative load. Therefore we denoted also in this case the expected stable singularities by symbol S and unstable by symbol N . The number following symbol S and N indicates the order of stable or unstable singularity, like in fig.1. Fig.3 shows projection of intersection $I-V$ planes into u_1, u_2 plane, together with BS cross-section for a current level of singularity $S3$ for $S3i = 2,28$ mA.

Fig.3 shows only three color areas instead of four - for attractors denoted by capital letters. According to fig.3 singularity $S3$ lies on the boundary of three regions of attractivity and therefore it can not be stable. To verify the accuracy of the simulation there were calculated eigenvalues of all singularities with help of Matlab. All of them are listed in Table 1. From Table 1 it is clear that $S2$ (and similarly also $S1$ and $S4$) are really stable singularities - $\lambda_1, \lambda_2, \lambda_3 > 0$ and $N1$ (and similarly also $N2$ and $N3$) are unstable - saddle singularities because $\lambda_1 > 0$ and $\lambda_2, \lambda_3 < 0$.

Signs of eigenvalues for $S3$, however, show in comparison with $N2$ some inversion with the knowledge that singularity $S3$ can not be truly stable singularity. Thus, there arises a natural question of the impact of the negative differential resistance (NDR) area of the element and the load on the character of singularity formed by intersections of NDR areas when $R=0$ or $R>0$. There were proposed also another two structures this once the quaternary memory for comparison with previous example.

Table 1 - Eigenvalues of singularities when $R=100\Omega$

	S1
λ_1	-8.805658515540415e+11
λ_2	-4.041794221742470e+11
λ_3	-1.587162647332503e+11
	N1
λ_1	1.613194157448778e+11
λ_2	-3.912351708589386e+11
λ_3	-8.981611679628628e+11
	S2
λ_1	-3.442835209177844e+11
λ_2	-5.184580681185036e+11
λ_3	-8.422584109637107e+11
	N2
λ_1	1.889942627330766e+11
λ_2	-4.649302854271197e+11
λ_3	-8.890639773059575e+11
	S3
λ_1	2.087673466538174e+11
λ_2	1.355735634806396e+11
λ_3	-9.324178332113801e+11
	N3
λ_1	1.647994698193528e+11
λ_2	-4.857757784109082e+10
λ_3	-9.239141996705698e+11
	S4
λ_1	-1.459626569961180e+11
λ_2	-4.763648402352091e+11
λ_3	-8.703648104609810e+11

Quaternary memory

For creation $I-V$ characteristics of the second structure was used the same relation (2) with following parameters:

$$\begin{aligned}
 &{}^1g_0=0,0833; \quad {}^1g_1=-0,0571; \quad {}^1g_2=-0,012; \quad {}^1g_3=0,0281; \\
 &{}^2g_0=0,0833; \quad {}^2g_1=-0,0571; \quad {}^2g_2=-0,012; \quad {}^2g_3=0,0281 \text{ [S]}; \\
 &{}^1U_1=60; \quad {}^1U_2=120; \quad {}^1U_3=250; \quad {}^2U_1=60; \quad {}^2U_2=120; \\
 &{}^2U_3=250 \text{ [mV]}. \quad (4)
 \end{aligned}$$

and for creation $I-V$ characteristics of the third structure was also used relation (2) with parameters:

$$\begin{aligned}
 &{}^1g_0=0,0833; \quad {}^1g_1=-0,0571; \quad {}^1g_2=-0,012; \quad {}^1g_3=0,0281; \\
 &{}^2g_0=0,1; \quad {}^2g_1=-0,05; \quad {}^2g_2=-0,001; \quad {}^2g_3=0,032 \text{ [S]}; \\
 &{}^1U_1=60; \quad {}^1U_2=120; \quad {}^1U_3=250; \quad {}^2U_1=50; \quad {}^2U_2=145; \\
 &{}^2U_3=250 \text{ [mV]}. \quad (5)
 \end{aligned}$$

Projections the $I-V$ characteristics into the plane i, u_2 for $R=0$ and parameters (4) or (5) are shown in fig. 4a) or fig.5a). In both cases the singularity $N2$, similarly as

in fig.3 when $R=100\Omega$, consists of two segments with the NDR. Singularities denoted with the letter S or N are stable or unstable and the number indicates their order like in fig.1.

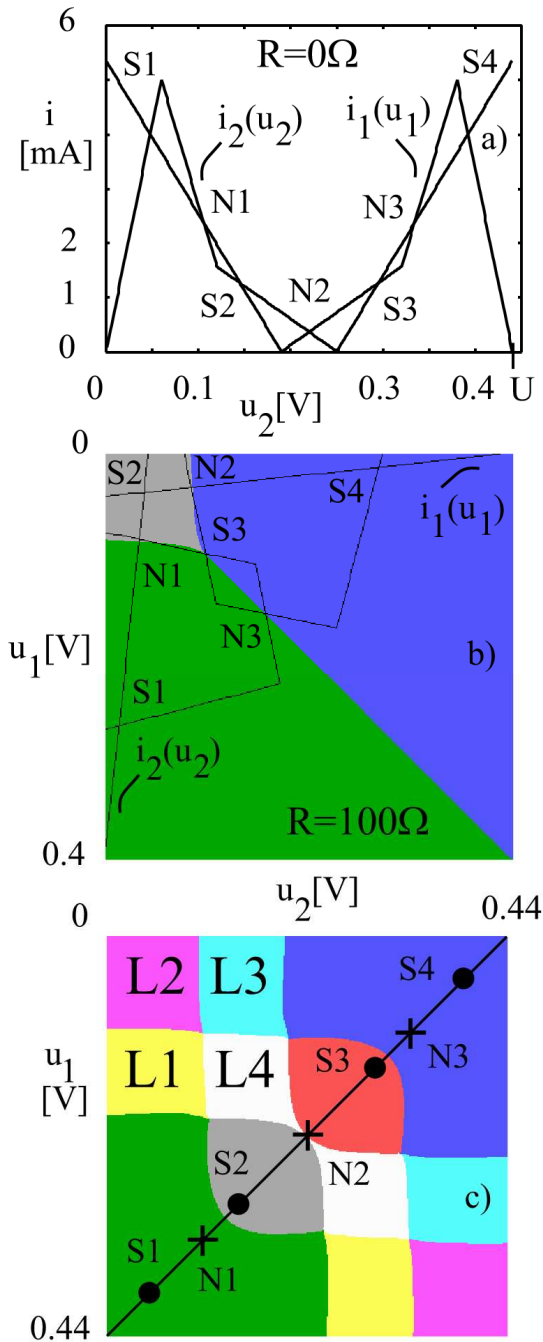


Figure 4 a) $I-V$ characteristics in the projection into the i, u_2 plane at $R=0$, b) The projection of cross-sections of $I-V$ areas into the plane u_1, u_2 at $R=100\Omega$, and cross-section of S3 for $^{S3}i=2,23$ mA. c) Cross-sections of BS in plane u_1, u_2 at $i=0$ mA, $R=0$.

In both cases it is evident alternation of singularities: stable – unstable – stable, etc. Even singularity N2 we designed so, that it consists of two segments of $I-V$ characteristics with NDR. Eigenvalues of singularity N2 in fig.4a, for parameters: $R=0, L=1e-10H, C=2,6e-13F$ (6)

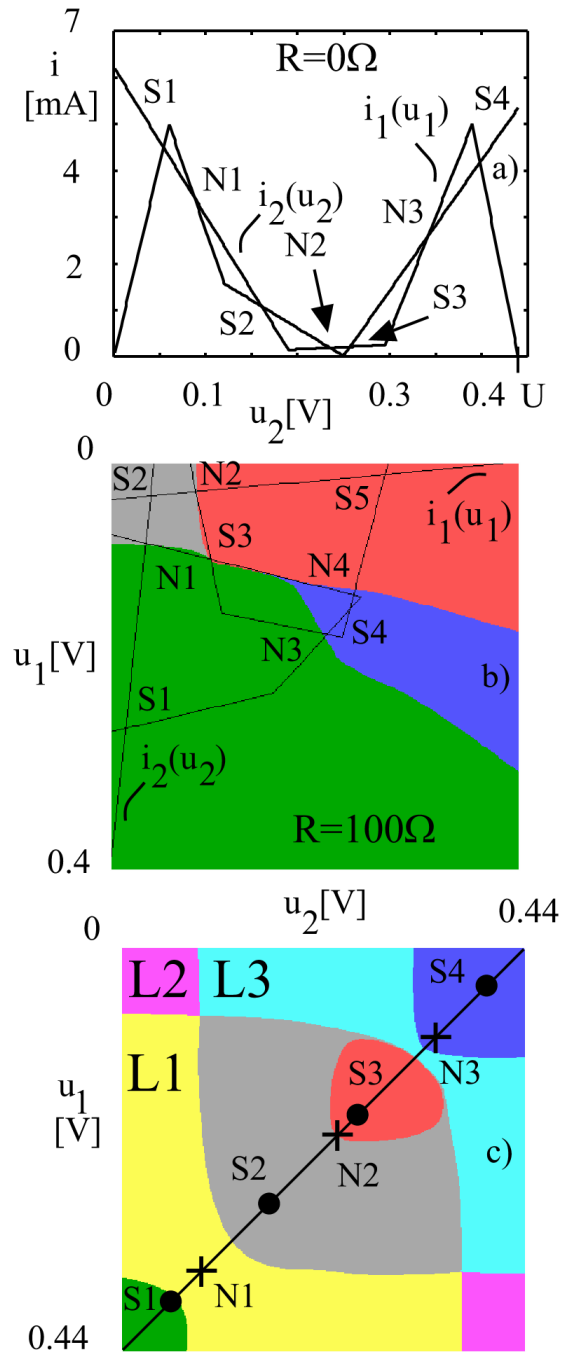


Figure 5 a) $I-V$ characteristics in the projection into the i, u_2 plane at $R=0$, b) The projection of cross-sections of $I-V$ areas into the plane u_1, u_2 at $R=100\Omega$, and cross-section of S3 for $^{S3}i=2,28$ mA. c) Cross-sections of BS in plane u_1, u_2 at $i=0$ mA, $R=0$.

are listed in Table 2. As we can see, N2 is not a classic saddle singularity, because it has all their real parts of eigenvalues greater than 0. The others N1 and N3 in fig.4a) or fig.5a) are characterized by the same structure of eigenvalues.

Cross-sections of BS for $R=0$, parameters (6) and $i=0$ in the plane u_1, u_2 were done by the same algorithm mentioned in [1], [14], [15], like in the fig. 3. From these cross-sections are obvious regions of attractivity

for particular attractors – stable singularities S1 ÷ S4 and undesirable SLC L1 ÷ L4.

Table 2 Eigenvalues for an unstable singularity N2

	N2
λ_1	4.615384615384615e+010
$\lambda_{2,3}$	2.307692307692307e+010 ± ±2.763883726649490e+011i

Regions of attractivity in fig. 4c) and fig. 5c) for stable singularities S1 ÷ S4 are indicated by placing the symbol (S1 ÷ S4) in the color field, whereas the symbol ● corresponds to the actual position of stable singularity in u_1, u_2 plane. The symbol + in the same figures denotes the actual positions of saddles N1 ÷ N3.

As can be seen from both figures, quaternary memory at $R=0$ is characterized not only by four regions of attractivity for S1 ÷ S4, but also by other regions of attractivity of undesirable SLC denoted L1 ÷ L4. Because the structure in fig.4a) is symmetrical, regions of attractivity in fig. 4c) are in the plane u_1, u_2 distributed symmetrically in comparison with fig.5a) and fig.5c). Interesting is also finding that in fig. 4c) are shown regions of attractivity for 4 SLC, but in fig. 5c) are only 3 SLC. Effect of change even only one segment of the $I-V$ characteristic in this case is obvious.

If we choose $R=100\Omega$, so from fig.4b) and fig.5b) we can see that SLC ceased to exist and circuits have only a few areas of attractivity. In fig.4b) at $R=100\Omega$, the number of singularities did not increase. Increasing the number of singularities for $R>0$ therefore depends on properly designed $I-V$ characteristics. Circuit in fig.4b) is no longer characterised by four, but only by three regions of attractivity for attractors S1, S2 and S4, because singularity S3 is again unstable, which confirms fig.4b), but also their eigenvalues from Matlab listed in Table 3. Thus from four-valued (quaternary) memory for $R=0$ we obtained for $R=100\Omega$ again only ternary memory.

Table 3 Eigenvalues for an unstable singularity S3

	S3
λ_1	2.196153846153846e+011
λ_2	1.528936382449410e+011
λ_3	-9.332782536295563e+011

A similar situation occurred also for the structure in fig.5. While at $R=0$ were in the circuit four attractors (S1 ÷ S4), at $R=100\Omega$ the number of singularities increased. If it would hold alternation of singularities stable - unstable - stable, etc., as at $R=0$, then at $R=100\Omega$ we would obtain even five stable singularities. But reality resulting from fig.5b) is quite different. Singularity S3 again becomes unstable, because it lies exactly on the boundary of three regions of attractivity. Number of stable singularities, namely S1, S2, S4 and S5, in this case remains unchanged. It is the same for $R=100\Omega$ and for $R=0$.

From both structures in fig.4 and fig.5 it is clear that when $R=100\Omega$, there was created singularity, which in terms of alternating singularities (stable – unstable) should be stable, but it was shown that, like in fig. 3, it

is unstable singularity. Then, the final number of stable singularities is not greater than would appear based on conventional notions for $R=0$.

Conclusion. Based on mentioned findings we can conclude that if by influence of R tilting the $I-V$ planes of the elements occurs so that there is not only the multiplication of singularities, but also to the fact that singularities are created by segments with negative differential resistance, then it will apply that:

- the number of unstable singularities will be greater than the number of stable singularities,
- it is not possible due to the impact of the loading plane R to expect changing singularities (stable - unstable) in terms of projections into the i, u_2 or i, u_1 plane, nor even from the perspective of the load plane.

In the case of utilization changes in the number of singularities in sequential circuits, the circuit should be subjected to exact analysis as it was also in the works [1], [4], [16], [17]. In terms of non-linear circuits and state space are not ruled out other interesting publication areas and topics of interest for international cooperation.

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REFERENCES

- 1 Špány, V., Galajda, P., Guzan, M., Boundary Surfaces of One-port Memories, 5th International Conference Tesla Millennium, Beograd, 1996, October 15-18, pp.130-137.
- 2 Galajda, P., Guzan, M., Špány, V., The state space mystery with negative load, Radioengineering, 1999, vol. 8, no 2, pp. 2-7.
- 3 Guzan, M., Limit cycles and trajectories in multivalued memory cell, Acta Electrotechnica et Informatica. Vol. 2, no. 1 (2002), pp. 36-41. - ISSN 1335-8243 (in Slovak)
- 4 Galajda, P., Guzan, M., Špány, V., The state space description of the MVL memory circuits, Education, Science and Economics at Universities: integration to international educational AREA: international conference: September 20-25, 2010, Plock, Poland. - Plock: Wydawnictwo Naukowe NOVUM, 2010 pp. 351-359. - ISBN 978-83-60662-38-0
- 5 Galajda, P., Non-linear system applied in the circuit theory. 4th International TEMPUS Telecomnet Workshop ITTW'98, July 1998, Barcelona, Spain, pp. 135-139.
- 6 Galajda, P., Špány, V., Guzan, M., The state space mystery with virtual saddle point in memory cell. DSP-MCOM 2005, 6th International Conference on Digital Signal Processing and Multimedia Communications, sept. 2005, Košice, pp. 147 - 150.

7 Guzan, M., Boundary surface and stable manifold in sequential circuits, Radioelektronika 2011: Proceedings of 21th international conference: April 19-20, 2011, Brno, Czech Republic: Brno University of Technology, 2011 P. 219-222. - ISBN 978-1-61284-323-0

8 Galajda, P., Guzan, M., Špány, V., The Control of a Memory Cell with the Multiple Stable States, Radioelektronika 2011: Proceedings of 21th international conference: April 19-20, 2011, Brno, Czech Republic: Brno University of Technology, 2011 pp. 211-214. - ISBN 978-1-61284-322-3

9 Guzan, M., Kollár, M., The load plane and singularities in MVL circuit, Radioelektronika, Brno, 2003, pp. 1-4.

10 Kováč, D., Vince, T., Molnár, J.: Modern Internet Based Production Technology. In: New Trends in Technologies, SCIYO Publisher, Croatia, 2010, 20 pages, ISBN 978-953-307-212-8

11 Krehel, R., Sensoren in der Prozessautomation und Prozessinformatik. CO-MAT-TECH 2004 : 12th International Scientific Conference, Trnava, Slovak republic, 14.-15. October 2004. Bratislava : STU Bratislava, 2004. s. 665- 672. ISBN 80-227-2117-4.

12 Vince, T., Kováč, D: Remote and regulation of electrotechnical systems via Internet (in Slovak).

Publisher: FEI TU Košice 2010, 1st edition, 130 pages, ISBN 978-80-553-0571-4

13 Krehel, R., Prediction value limit state dynamic determinants of delay in the discrete control processes. Operation and diagnostics of machines and production systems operational states. Brno : Tribun EU, 2008. p. 108-112. ISBN 978-80-7399-634-5

14 Špány, V. Graphical solution of nonlinear circuit by m-dimensional phase space method. Elektrotechnický časopis, XX, 1969, No.4, pp.233-248. (in Slovak)

15 Špány, V., Pivka, L.: Boundary surfaces in sequential circuits. International journal of circuit theory and applications, vol.18, 1990, pp.349-360.

16 Špány, V., Galajda, P., Guzan, M., Pivka, L., Olejár, M., Chua's singularities: Great miracles in circuit theory, International Journal of Bifurcation and Chaos (IJBC). Vol. 20, no. 10 (2010), pp. 2993-3006. - ISSN 0218-1274

17 Petržela, J., Modeling of the Strange Behavior of the Selected Nonlinear Dynamical Systems. Part I.: Oscillators, Edition PhD Thesis, Vol. 502, ISSN 1213-4198.

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ГРАНИЧНАЯ ПОВЕРХНОСТЬ И УРОВЕНЬ НАГРУЗКИ ТЕРНАРНОЙ ПАМЯТИ

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В статье показано воздействие уровня нагрузки γ на характер сингулярностей многостабильной (многозначной) памяти. При $\gamma > 0$ в цепи с двумя последовательно соединенными резонансными туннельными диодами имеет место изменение числа сингулярностей. Таким образом, бинарная или многостабильная память может быть создана лишь за счет изменения γ . Однако, эксперименты указывают, что увеличение общего числа сингулярностей в схеме не всегда означает увеличение числа устойчивых сингулярностей.

Ключевые слова: пространство состояний, уровень нагрузки, сингулярность, тернарная память, резонансный туннельный диод

ГРАНИЧНА ПОВЕРХНЯ І РІВЕНЬ НАВАНТАЖЕННЯ ТЕРНАРНОЇ ПАМ'ЯТІ

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У статті показано вплив рівня навантаження γ на характер сингулярностей багатостабільної (багатозначної) пам'яті. При $\gamma > 0$ в ланцюзі з двома послідовно з'єднаними резонансними тунельними діодами має місце зміна числа сингулярностей. Таким чином, бінарна або багатостабільна пам'ять може бути створена лише за рахунок зміни γ . Однак, експерименти вказують, що збільшення загального числа сингулярностей у схемі не завжди означає збільшення числа стійких сингулярностей.

Ключові слова: простір станів, рівень навантаження, сингулярність, тернарна пам'ять, резонансний тунельний діод