

CIRCUIT SIMULATION IN MATLAB

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In this paper a circuit simulation of linear electric systems containing independent and dependent sources of DC voltage and/or current is proposed. For circuit simulation is used nodal analysis method and the program based on this method was created in MATLAB environment. The symbolic representation of constraint equations for node voltages and nodal equations for non reference nodes and supernodes is drawn up after setting of prescribed input data and the reference node. Symbolic representation of nodal voltages, branch voltages and branch currents and calculation their numeric values for given circuit element parameters can be also generated.

Key words: circuit simulation, linear electric circuit, nodal analysis, incidence matrix, MATLAB.

МОДЕЛЮВАННЯ ЕЛЕКТРИЧНИХ КІЛ У СЕРЕДОВИЩІ MATLAB

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Розглядається моделювання лінійних електричних систем, які містять незалежні та залежні джерела постійної напруги та/або струму. Для моделювання електричних кіл використовується метод вузлових потенціалів. Програму симуляції, яка базується на використанні даного методу, розроблено в середовищі MATLAB. Символьне надання рівнянь зв'язку для вузлів напруги та вузлових рівнянь для незв'язаних вузлів та «супервузлів» виконується після встановлення прийнятих вхідних даних та вузлів відношень. Також можуть бути розраховані символічне представлення вузлових напруг, напруг гілок та струмів гілок, а також обчислення їх чисельних значень для заданих параметрів елементу електричного кола.

Ключові слова: моделювання електричних кіл, лінійні електричні кола, вузловий аналіз, матриця подій, MATLAB.

PROBLEM STATEMENT. Circuit simulation is a technique for checking and verifying the design of electrical and electronic circuits and systems before to manufacturing and deployment [1]. It combines three important areas of research: mathematical modeling of the circuit elements, formulation of the circuit equations and techniques for solution of these equations.

This paper deals with formulation of the circuit equations. For circuit simulation on a computer it is necessary to use general-purpose methods which work for all circuits and formulation of the circuit equations must be done in systematic and automatic way for every circuit (large or small).

As the general method for symbolic analysis was used nodal analysis and as the programming environment was selected MATLAB environment.

Nodal analysis is method for solving linear electric circuits. Unknown quantities are nodal voltages. A nodal voltage is a voltage between a node pair, which is formed by a nonreference node and a reference node. A node is a point of connection of two or more circuit elements. Because MATLAB (MATrix LABoratory) is a tool for matrix solving of problems, it is necessary to create a matrix formulation of nodal analysis.

We assume a proper linear electric circuit containing N_u nodes and N_v branches. Let us consider a resistor with resistance $R_k \neq 0 \wedge R_k \neq \infty$, an ideal current source (independent or dependent) with a current $i_k^{(S)}$ and an ideal voltage source with a voltage $u_k^{(S)}$ are connected in a branch v_k ($k=1,2,\dots,n_v$) having the node p and node q at each end (Fig. 1).

KCL written in the matrix form is [2]:

$$\mathbf{A}\mathbf{I}^{(v)} = \mathbf{0}, \tag{1}$$

where $\mathbf{I}^{(v)}$ is a matrix of branch currents $i_k^{(v)}$ of order $(N_v, 1)$, \mathbf{A} being an incidence matrix of order $(N_u - 1, N_v)$ with elements a_{ij} which express incidence between nodes and branches of circuit and their value is $\{+1, -1, 0\}$.

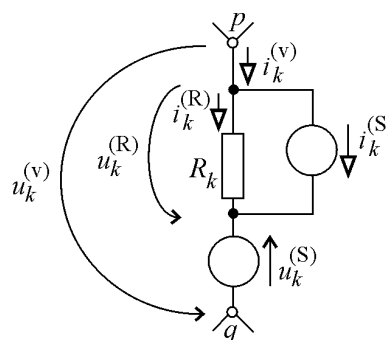


Figure 1 – The k th circuit branch connected the node p and the node q

The branch current $\mathbf{I}^{(v)}$ can be expressed:

$$\mathbf{I}^{(v)} = \mathbf{I}^{(R)} + \mathbf{I}^{(S)}, \tag{2}$$

where $\mathbf{I}^{(R)}$ is a matrix of resistor currents $i_k^{(R)}$ of order $(N_v, 1)$, $\mathbf{I}^{(S)}$ being a matrix of current source currents $i_k^{(S)}$ of order $(N_v, 1)$.

The resistor current $\mathbf{I}^{(R)}$ will be:

$$\mathbf{I}^{(R)} = \mathbf{G}(\mathbf{U}^{(v)} + \mathbf{U}^{(S)}), \quad (3)$$

where \mathbf{G} is a diagonal matrix of resistor conductance of order (N_v, N_v) , $\mathbf{U}^{(v)}$ being a matrix of branch voltages of order $(N_v, 1)$, $\mathbf{U}^{(S)}$ being a matrix of voltage source voltages of order $(N_v, 1)$.

Each branch voltage can be expressed in terms of a nodal voltage [1]:

$$\mathbf{U}^{(v)} = \mathbf{A}^T \mathbf{U}^{(n)}, \quad (4)$$

where \mathbf{A}^T is a transpose matrix to a matrix \mathbf{A} , $\mathbf{U}^{(n)}$ being a matrix of nodal voltages of order $(N_u - 1, 1)$.

Substituting equations (2), (3) and (4) into equation (1), we obtain

$$\mathbf{A} \mathbf{G} \mathbf{A}^T \mathbf{U}^{(n)} + \mathbf{A} \mathbf{G} \mathbf{U}^{(S)} + \mathbf{A} \mathbf{I}^{(S)} = \mathbf{0}. \quad (5)$$

The equation (5) represents a matrix formulation of nodal equations in terms of incidence matrix \mathbf{A} for electric circuits containing current and/or voltage sources (independent and dependent).

Matrix $\mathbf{U}^{(S)}$ in the equation (5) is following:

$$\mathbf{U}^{(S)} = \mathbf{U}^{(IVS)} + \mathbf{U}^{(CCVS)} + \mathbf{U}^{(VCVS)}, \quad (6)$$

where $\mathbf{U}^{(IVS)}$ is a matrix of voltages independent voltage sources with a positive sign if its polarity is not oriented corresponding to a branch orientation, $\mathbf{U}^{(CCVS)}$ being a matrix of voltages of current-controlled voltage sources (CCVS), $\mathbf{U}^{(VCVS)}$ being a matrix of voltages of voltage-controlled voltage sources (VCVS).

Matrix $\mathbf{U}^{(CCVS)}$ in the equation (6) is following:

$$\mathbf{U}^{(CCVS)} = \mathbf{V}^{(CCVS)} \mathbf{I}^{(v)}, \quad (7)$$

where $\mathbf{V}^{(CCVS)}$ is a matrix of coupling coefficients between voltages CCVS and controlling currents with a positive sign if its polarity is not oriented corresponding to a orientation of branch in which is the source, and simultaneous a polarity of controlling current is corresponding to a orientation of branch in which it is placed.

Matrix $\mathbf{U}^{(VCVS)}$ in the equation (6) is following:

$$\mathbf{U}^{(VCVS)} = \mathbf{V}^{(VCVS)} \mathbf{U}^{(v)}, \quad (8)$$

where $\mathbf{V}^{(VCVS)}$ is a matrix of coupling coefficients between voltages CCVS and controlling voltages with a positive sign if its polarity is not oriented corresponding to an orientation of branch in which is the source, and simultaneous a polarity of controlling voltage is oriented corresponding to a orientation of branch in which it is placed.

Matrix $\mathbf{I}^{(S)}$ in the equation (5) is following:

$$\mathbf{I}^{(S)} = \mathbf{I}^{(ICS)} + \mathbf{I}^{(CCCS)} + \mathbf{I}^{(VCCS)}. \quad (9)$$

where $\mathbf{I}^{(ICS)}$ is a matrix of currents independent current sources with a positive sign if its polarity is oriented corresponding to a branch orientation, $\mathbf{I}^{(CCCS)}$ being

a matrix of currents of current-controlled current sources (CCCS), $\mathbf{I}^{(VCCS)}$ being a matrix of currents of voltage-controlled current sources (VCCS).

Matrix $\mathbf{I}^{(CCCS)}$ in the equation (9) is following:

$$\mathbf{I}^{(CCCS)} = \mathbf{V}^{(CCCS)} \mathbf{I}^{(v)}, \quad (10)$$

where $\mathbf{V}^{(CCCS)}$ is a matrix of coupling coefficients between currents CCCS and controlling currents with a positive sign if its polarity is oriented corresponding to an orientation of branch in which is the source, and simultaneous a polarity of controlling current is oriented corresponding to a orientation of branch in which it is placed.

Matrix $\mathbf{I}^{(VCCS)}$ in the equation (9) is following:

$$\mathbf{I}^{(VCCS)} = \mathbf{V}^{(VCCS)} \mathbf{U}^{(v)}, \quad (11)$$

where $\mathbf{V}^{(VCCS)}$ is a matrix of coupling coefficients between currents VCCS and controlling currents with a positive sign if its polarity is oriented corresponding to an orientation of branch in which is the source, and simultaneous a polarity of controlling current is oriented corresponding to a orientation of branch in which it is placed.

Substituting equations (6) up to (11) into equation (5) we obtain the matrix formulation of nodal equations for linear circuits having independent and/or dependent sources:

$$\mathbf{G}^{*(n)} \mathbf{U}^{(n)} = \mathbf{I}^{(n)}, \quad (12)$$

where $\mathbf{G}^{*(n)}$ is a node conductance matrix of order $(N_u - 1, N_u - 1)$, $\mathbf{I}^{(n)}$ being a column matrix of short-circuiting currents produced by the current sources and voltage sources of order $(N_u - 1, 1)$.

The obtained system of the nodal equations (12) must be treated for ideal current and voltage sources. An internal resistance of an ideal current source that is connected in the k th circuit branch is infinitely large and so its internal conductance is equal zero. An internal resistance of an ideal voltage source that is connected in the k th circuit branch is equal zero and so its internal conductance is infinitely large ($g_{kk} \rightarrow \infty$). For that reason it is necessary to arrange the system of nodal equation (12) as follows:

– if a voltage source is connected between non reference node p and reference node, a limit for $g_{kk} \rightarrow \infty$ of the left-hand side of equation written for nonreference node p must be found

$$\lim_{g_{kk} \rightarrow \infty} \left(\sum_{j=1}^{N_u-1} \frac{g_{pj}^{*(n)}}{g_{kk}} u_j^{(n)} \right), \quad (13)$$

as well as a limit for $g_{kk} \rightarrow \infty$ of right-hand side of equation written for nonreference node p

$$\lim_{g_{kk} \rightarrow \infty} \frac{i_p^{(n)}}{g_{kk}}, \quad (14)$$

and the constraint equation in the form

$$u_p^{(n)} = \pm u_k^{(S)} \quad (15)$$

will be obtained by this arrangement,

– if a voltage source is connected between a non reference node p and a non reference node q , a limit for $g_{kk} \rightarrow \infty$ of the both sides of equation written for nonreference node p must be found and the constraint equation in the form

$$u_p^{(n)} - u_q^{(n)} = \pm u_k^{(S)} \quad (16)$$

will be obtained by this arrangement; but one more equation is needed, which will be provided by a nodal equation for supernode $p - q$ in such a way, that we add the equation for nonreference node p and the equation for non reference node q

$$\sum_{j=1}^{N_u-1} (g_{pj}^{*(n)} + g_{qj}^{*(n)}) u_j^{(n)} = i_p^{(n)} + i_q^{(n)}. \quad (17)$$

If any coefficient $g_{pj}^{*(n)}$ in the equation (17) includes a term $\pm g_{kk}$ (independent voltage source) or a term $\pm \rho_k g_{kk} g_{ll}$ (CCVS) or a term $\pm \alpha_k g_{kk}$ (VCVS), then a coefficient $g_{qj}^{*(n)}$ includes a term $\mp g_{kk}$ or a term $\mp \rho_k g_{kk} g_{ll}$ or a term $\mp \alpha_k g_{kk}$, and there is no any coefficient with term g_{kk} in the equation (17).

EXPERIMENTAL PART AND RESULTS OBTAINED. After assigning an input data (parameters of circuit elements, the incidence matrix and the reference node) a program generates a symbolic system of nodal equations. This system is subsequently solved and the obtained results are in symbolic and numeric form. In next part a solving of a circuit containing independent and dependent sources will be presented.

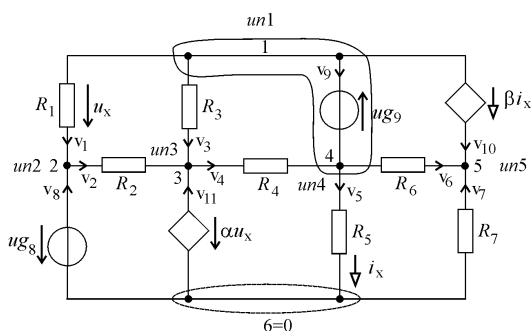


Figure 2 – Solved Circuit

Referring to Fig. 2, the program generates the system of equations (Fig. 3), which consists of three constraint equations and two nodal equations, one of them is for a non reference node 5 and the other one is for a supernode 1-4.

Constraint equations:

$$\begin{aligned} un2 &= ug8 \\ -alfa1*un1+alfa1*un2+un3 &= 0 \\ -un1+un4 &= ug9 \end{aligned}$$

Nodal equations for non reference nodes:

$$\text{node 5: } (-beta1*g5-g6)*un4+(g6+g7)*un5 = 0$$

Nodal equations for supernodes:

$$\text{supernode 1-4: } (g1+g3)*un1-g1*un2+(-g3-g4)*un3+(beta1*g5+g4+g5+g6)*un4-g6*un5 =$$

Figure 3 – Symbolic equations generated by program

For given values of circuit parameters: $R_1 = R_2 = R_3 = R_4 = R_5 = R_6 = R_7 = 1k\Omega$, $ug_8 = 12 \text{ V}$, $ug_9 = 6 \text{ V}$, $ig_{10} = \beta i_x = 2 i_x \text{ A}$, $ug_{11} = \alpha u_x = 2 u_x \text{ V}$, the program computes:
– the numeric values of nodal voltages (Fig. 4):

Nodal voltages - numeric values:

$$\begin{aligned} un1 &= -38.000000 \text{ V} \\ un2 &= 12.000000 \text{ V} \\ un3 &= -100.000000 \text{ V} \\ un4 &= -32.000000 \text{ V} \\ un5 &= -48.000000 \text{ V} \end{aligned}$$

Figure 4 – Nodal voltages

– the symbolic expressions of branch voltages in terms of nodal voltages and their numeric values (Fig. 5):

Branch voltages:

$$\begin{aligned} uv1 &= un1-un2 = -50.000000 \text{ V} \\ uv2 &= un2-un3 = 112.000000 \text{ V} \\ uv3 &= un1-un3 = 62.000000 \text{ V} \\ uv4 &= un3-un4 = -68.000000 \text{ V} \\ uv5 &= un4 = -32.000000 \text{ V} \\ uv6 &= un4-un5 = 16.000000 \text{ V} \\ uv7 &= -un5 = 48.000000 \text{ V} \\ uv8 &= -un2 = -ug8 = -12.000000 \text{ V} \\ uv9 &= un1-un4 = -ug9 = -6.000000 \text{ V} \\ uv10 &= un1-un5 = 10.000000 \text{ V} \\ uv11 &= -un3 = -2*uv1 = 100.000000 \text{ V} \end{aligned}$$

Figure 5 – Branch voltages

– the symbolic expressions of branch currents in terms of nodal voltages and their numeric values (Fig. 6):

Branch currents:

$iv1 = (un1-un2)*g1 = -0.050000 \text{ A}$
 $iv2 = (un2-un3)*g2 = 0.112000 \text{ A}$
 $iv3 = (un1-un3)*g3 = 0.062000 \text{ A}$
 $iv4 = (un3-un4)*g4 = -0.068000 \text{ A}$
 $iv5 = un4*g5 = -0.032000 \text{ A}$
 $iv6 = (un4-un5)*g6 = 0.016000 \text{ A}$
 $iv7 = -un5*g7 = 0.048000 \text{ A}$
 $iv8 = -iv1+iv2 = 0.162000 \text{ A}$
 $iv9 = -iv1-iv3-iv10 = 0.052000 \text{ A}$
 $iv10 = 2*iv5 = -0.064000 \text{ A}$
 $iv11 = -iv2-iv3+iv4 = -0.242000 \text{ A}$

Figure 6 – Branch currents

CONCLUSIONS. The paper is aimed at matrix formulation of nodal analysis for linear electric circuits having independent and/or dependent sources. The program based on this method was created in MATLAB environment. The mentioned program generates symbolic expression of equations which are needed for

nodal analysis of circuit, symbolic expressions and numeric values of branch voltages and branch currents in terms of nodal voltages. It is confirmed the fact that MATLAB is considered to provide a powerful and flexible environment also for programs.

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REFERENCES

1. Najm F.N. *Circuit Simulation*. – New Jersey: John Wiley & Sons, Inc., 2010. – 318 p.
2. Dorf R.C., Svoboda J.A.: *Introduction to Electric Circuits*. – New Jersey: John Wiley & Sons, Inc., 2007. – 854 p.
3. Mayer, D. *Introduction to Theory of Electric Circuits*. – Prague: SNTL, 1984 – 854 p. [in Czech]
4. MATLAB – *User's Guide*, MathWorks, 2009.

МОДЕЛИРОВАНИЕ ЭЛЕКТРИЧЕСКИХ ЦЕПЕЙ В СРЕДЕ MATLAB

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Рассматривается моделирование электрических систем, содержащих независимые и зависимые источники постоянного тока и/или напряжения. Для моделирования электрических цепей используется метод узловых потенциалов. Программа симуляции, основанная на использовании данного метода, разработана в среде MATLAB. Символьное представление уравнений связи для узлов напряжения и узловых уравнений для несвязанных узлов и «суперузлов» выполняется после установления принятых входных данных и узловых соотношений. Также могут быть рассчитаны символьное представление узловых напряжений, напряжений ветвей и токов ветвей, а также вычисления их числовых значений для заданных параметров элемента электрической цепи.

Ключевые слова: моделирование электрических цепей, линейные электрические цепи, узловой анализ, матрица событий, MATLAB.

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